

MAY 15 2006

PTO/SB/21 (09-04)

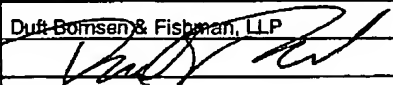
Approved for use through 07/31/2006. OMB 0851-0031

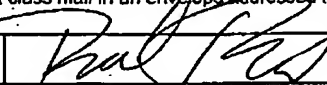
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/743,957	
	Filing Date	Dec 23, 2003	
	First Named Inventor	Grimes, Gordon K.	
	Art Unit	2863	
	Examiner Name	M. Walling	
Total Number of Pages in This Submission	7	Attorney Docket Number	03-0376

ENCLOSURES (Check all that apply)		
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Firm Name	Duft Bornszen & Fishman, LLP		
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Printed name	Daniel Fishman		
Date	May 15, 2006	Reg. No.	35512

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LSI Docket No. 03-0376

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Application Serial No. : 10/743,957
Filed : 23 December 2003
Applicant : Grimes, G. et al.
Title : Systems and Methods for Assessing Timing of PCI
Signals
Art Unit : 2863
Examiner : Walling, M.
Docket Number : 03-0376
Date : 15 May 2006

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the Office Action mailed 16 February 2006, please consider the following remarks.

Remarks

In the non-final Office Action mailed on 16 February 2006, the Examiner rejected claims 1-3, 8-13, 16-18, and 20 under 35 U.S.C. §102(e) as anticipated by Gates (United States Patent Number 6,516,366). The Examiner objected to claims 4, 6-7, 14-15, and 19 but found them allowable if appropriately re-written.

Applicant respectfully traverses the Examiner's rejection and requests reconsideration and withdrawal of same.

35 U.S.C. §102 Rejection

The Examiner rejected claims 1-3, 8-13, 16-18, and 20 as anticipated by Gates. In particular, the Examiner reads in claim 1 the recited step of initiating a test mode in a host adapter is read by the Examiner at Gates column 11, lines 23-26. The recited step of generating a clock signal for the host adapter is read by the Examiner generally pointing to Gates' figure 2A and the CLK40 signal therein. The step of generating PCI signals is read by the examiner as support circuit 250 of figure 2A associated with host adapted 240